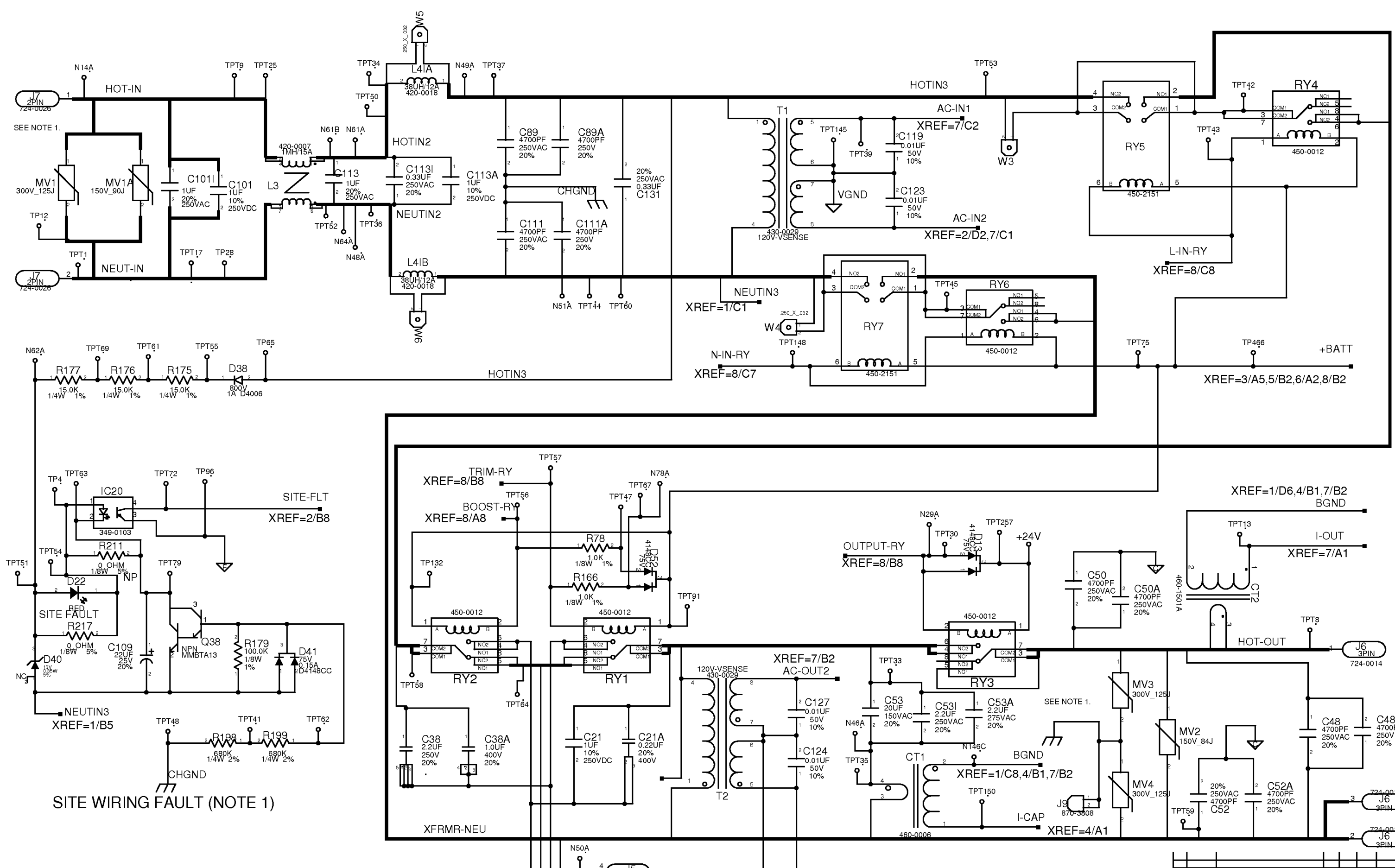


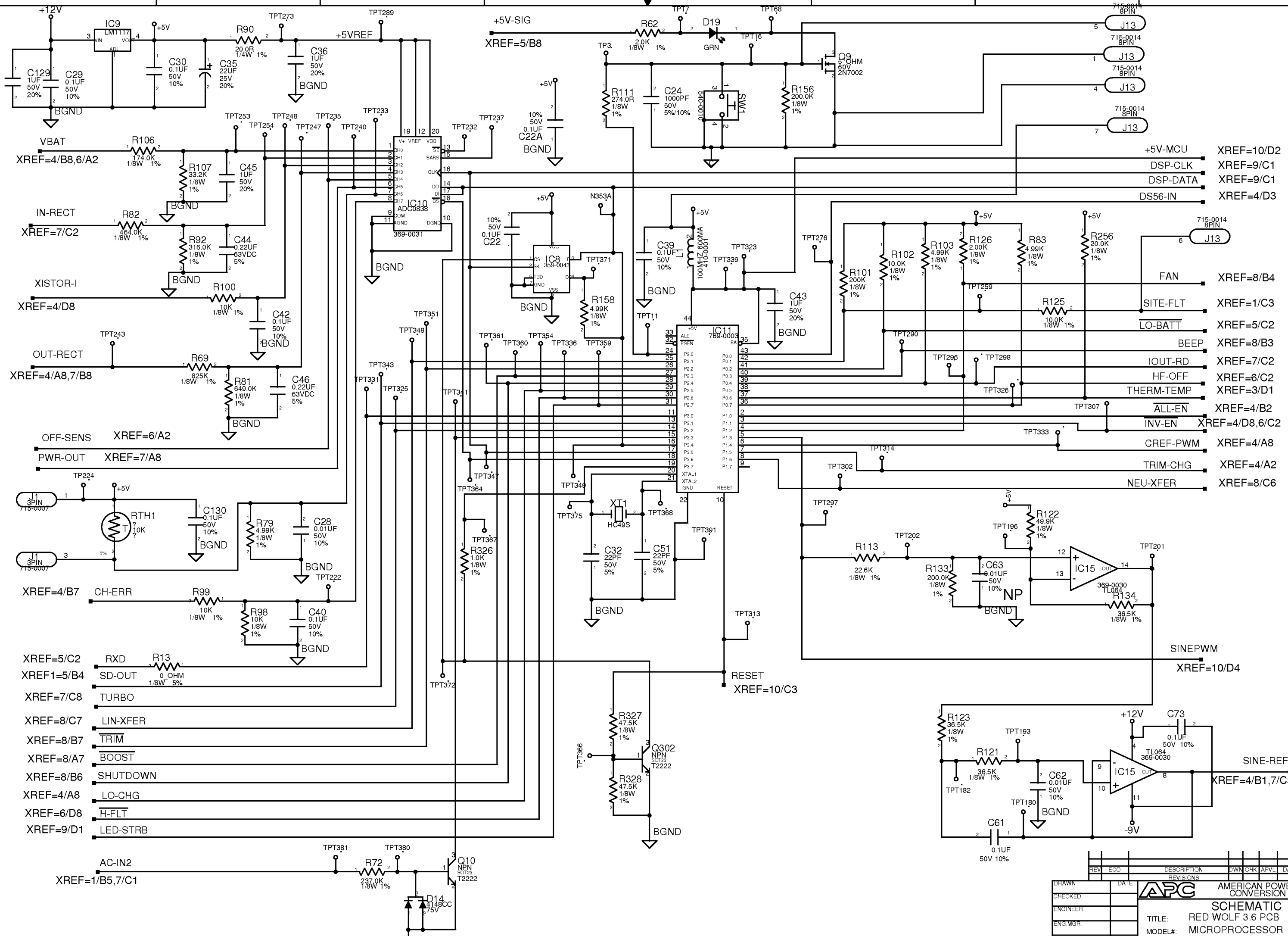


NOTE 1.
The BOM controls component values, quantities and reference designators for assemblies built from this circuit schematic.
NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

REV		ECO	DESCRIPTION	OWN	CHK	APVL	DATE
1			SCHEMATIC				
2			RED WOLF 3.6 PCB				
3			POWER				
4			7732H				
5			7732H				
6			7732H				
7			7732H				
8			7732H				
9			7732H				
10			7732H				

SCALE: NONE FILENAME: SHEET 1 OF 10

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NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

REV	ECO	DESCRIPTION	DWN	CHK	APVL	DATE
1		SCHEMATIC				
2		RED WOLF 3.6 PCB				
3		MICROPROCESSOR				
4		7732H				
5						
6						
7						
8						
9						
10						

DRAWN

CHECKED

ENGINEER

ENG MGR

QUALITY

PRODUCTION

DATE

SCALE: NONE

FILE NAME

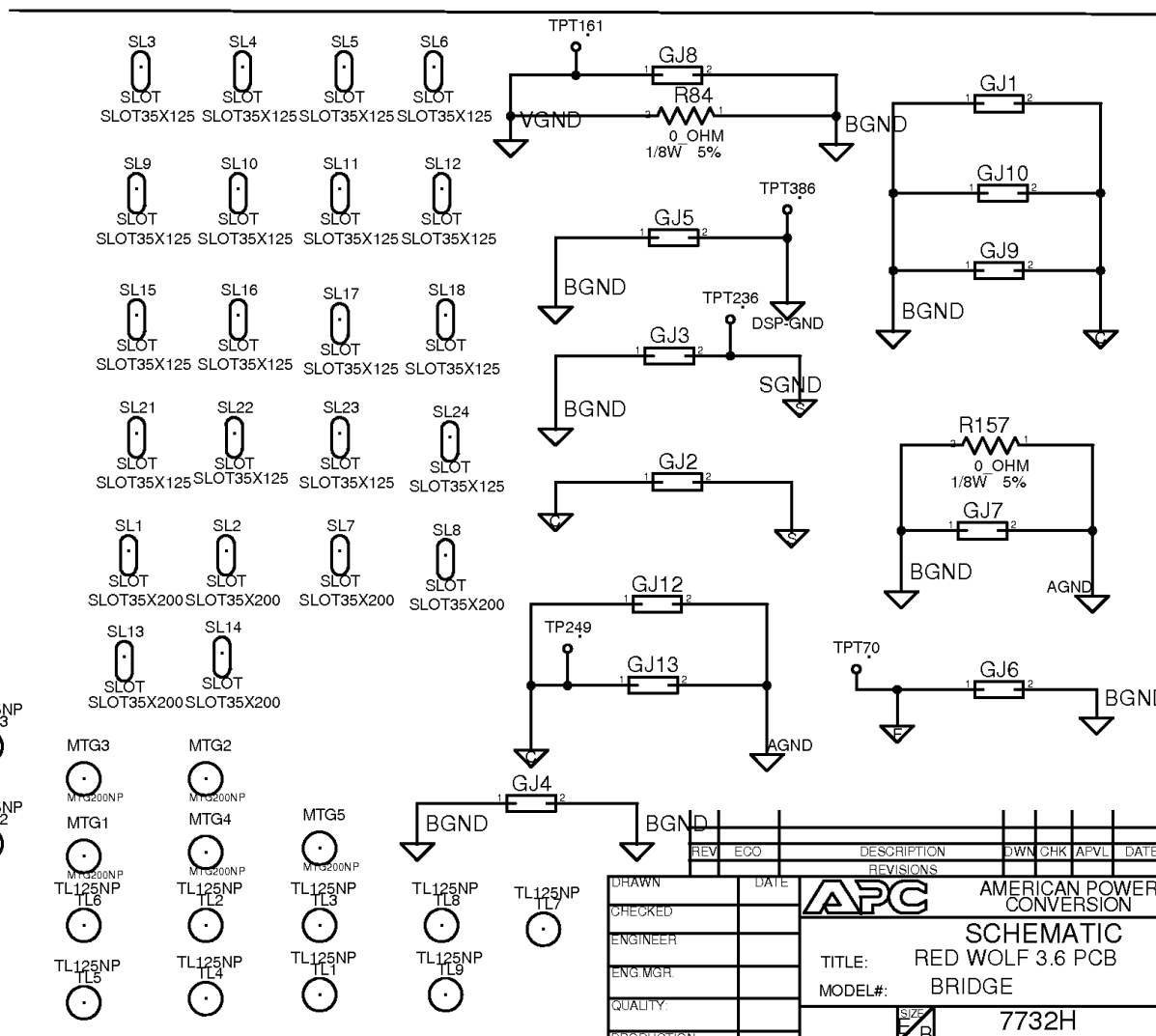
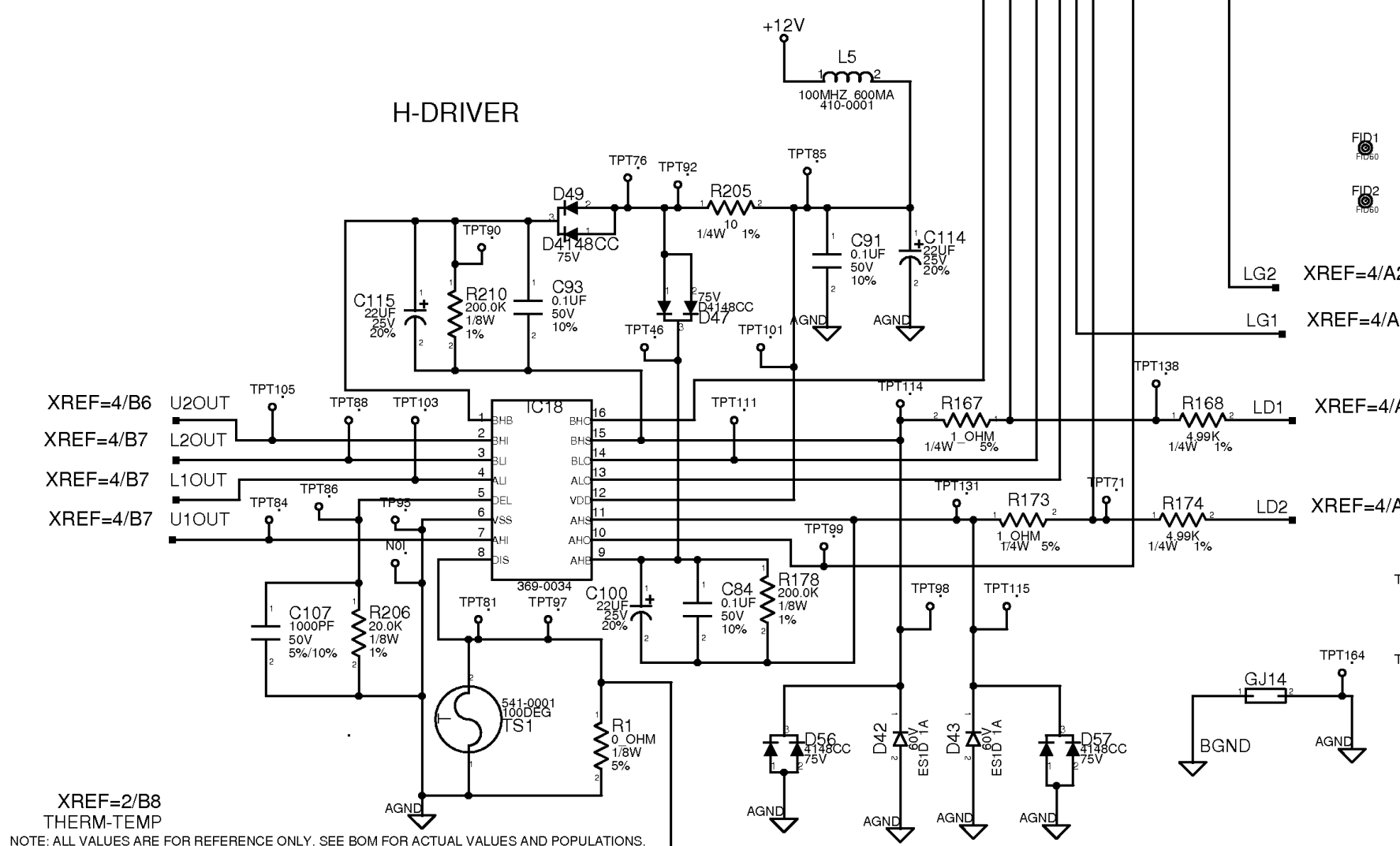
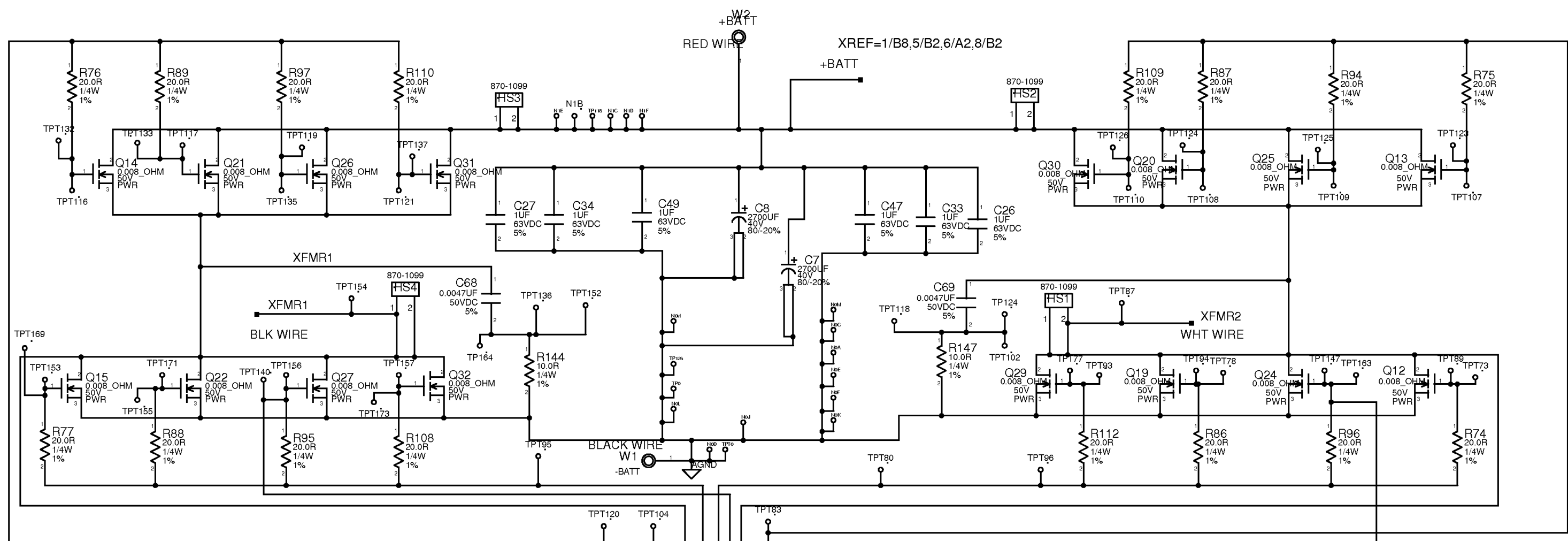
SHEET 2 OF 10

AMERICAN POWER CONVERSION

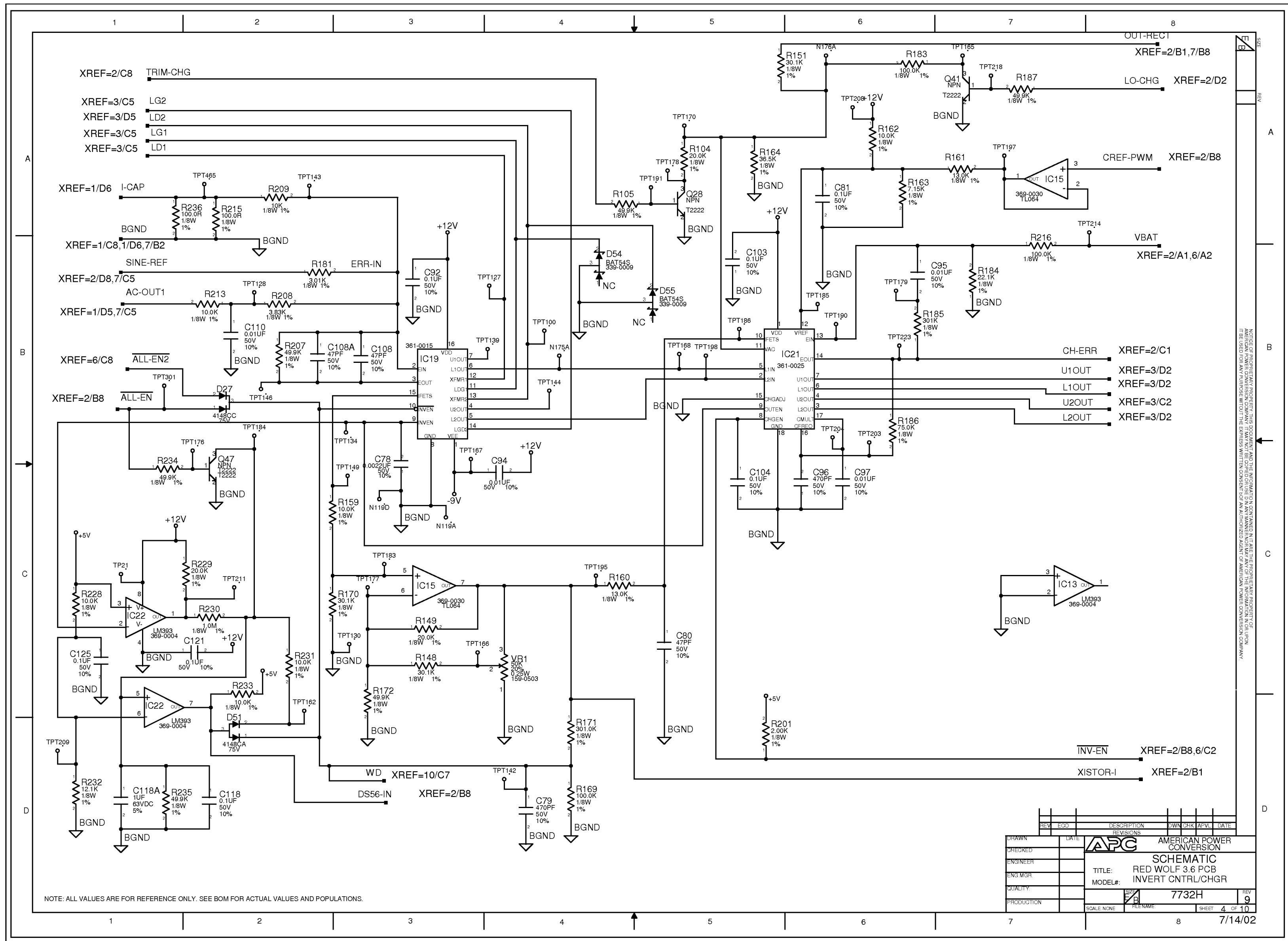
7732H

7/14/02

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REV	ECO	DESCRIPTION	DWN	CHK	APVL	DATE
1		SCHEMATIC				
2		RED WOLF 3.6 PCB				
3		BRIDGE				
4		MODEL#				
5		7732H				
6		REV				
7		9				
8		SCALE: NONE				
9		FILE NAME				
10		SHEET				
11		3 OF 10				



P	N	SIGNAL NAME
1	1	+12V
2	2	SGND
3	3	+24V-OUT
4	4	SGND
5	5	+5V
6	6	SDI-AS1
7	7	SDI-ACC1
8	8	SDI-OUT1
9	9	LO-BATT1
10	10	DIVERT1
11	11	SDO-ACC1
12	12	ON-SW
13	13	OFF-SW
14	14	+

XREF=5/A1

XREF=5/A7, 6/B2, 9/C1
XREF=5/B8, 6/B2, 9/D1

+BATT (SHORT to +BATT thru FUSE)
+BATT
XREF=1/B8, 3/A5, 6/A2, 8/B2

XREF=2/C2 SD-OUT

XREF=2/C2 RXD

XREF=2/B8 LO-BATT

USB SERIAL DATA OUT

USB SERIAL DATA IN

MUX CNTRL

NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

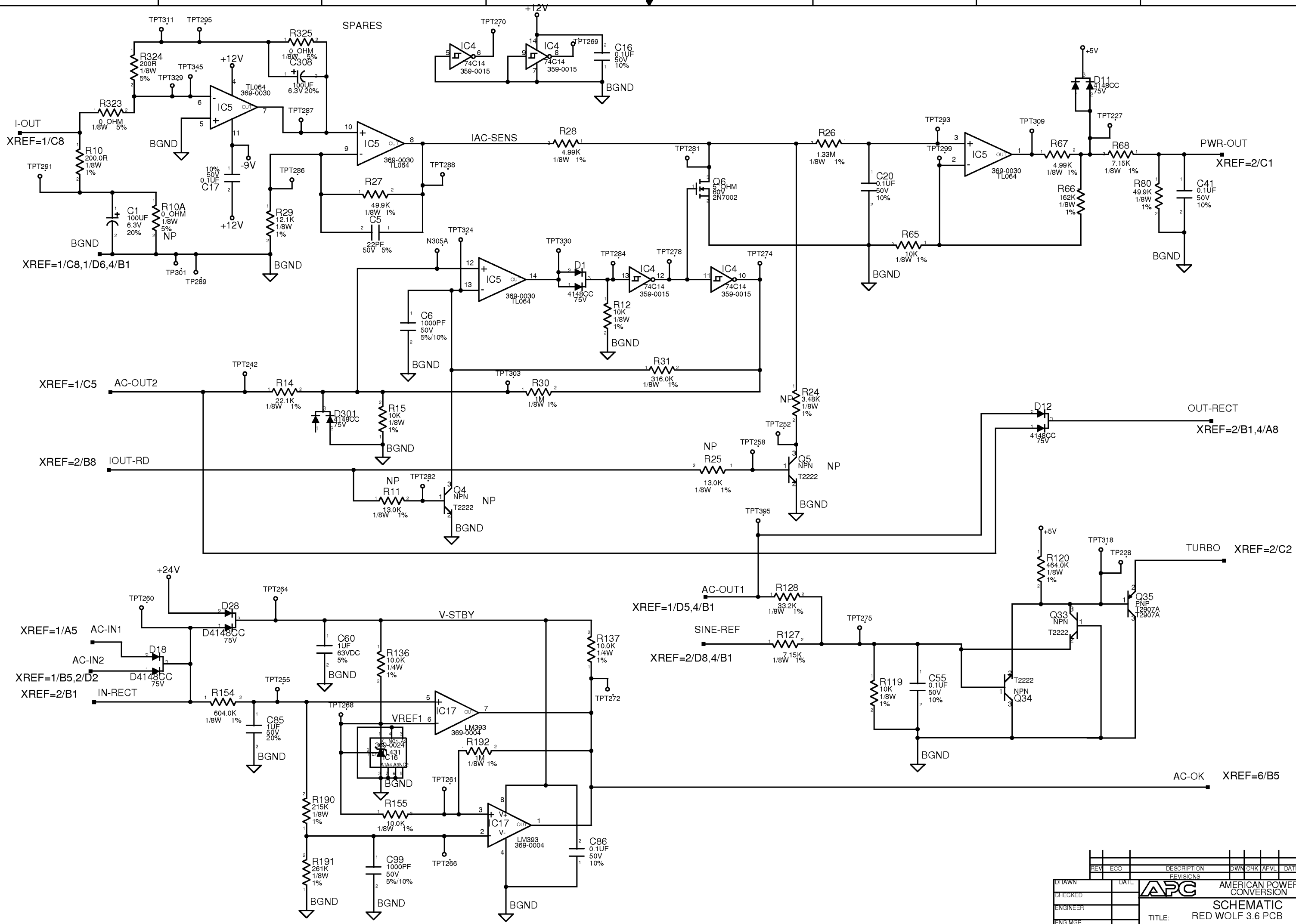
REV	ECO	DESCRIPTION	DWN	CHK	APVL	DATE
1						
2						
3						
4						
5						
6						
7						
8						
9						
10						

DRAWN	DATE	DESCRIPTION	DWN	CHK	APVL	DATE
CHECKED						
ENGINEER						
ENG MGR						
QUALITY						
PRODUCTION						

AMERICAN POWER CONVERSION	
SCHEMATIC	
RED WOLF 3.6 PCB	
SMARTSLOT/DB9	
TITLE:	MODEL#:
7732H	9
SCALE: NONE	FILE NAME:
SHEET	5 OF 10

7/14/02

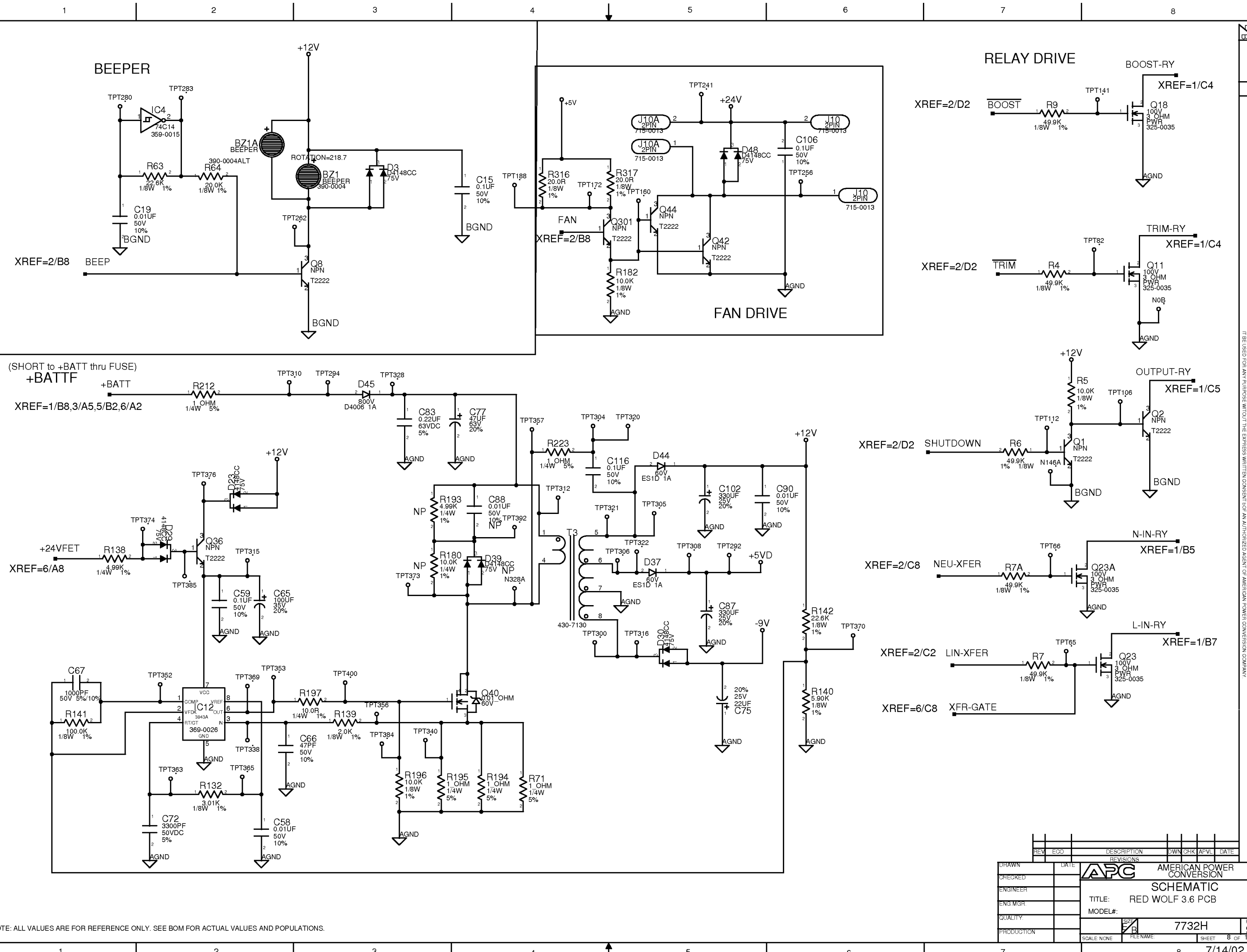
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REV	ECO	DESCRIPTION	OWN	CHK	APVL	DATE
1						
2						
3						
4						
5						
6						
7						
8						
9						
10						

DRAWN	DATE	APC	AMERICAN POWER CONVERSION
CHECKED			
ENGINEER			
ENG. MGR.			
QUALITY			
PRODUCTION			
TITLE: RED WOLF 3.6 PCB		MODEL#: 7732H	
SCALE: NONE		SHEET: 7 OF 10	



NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

REV		ECO	DESCRIPTION	OWN	CHK	APVL	DATE
DRAWN							
CHECKED							
ENGINEER							
ENGR MGR							
QUALITY							
PRODUCTION							
SCALE: NONE		FILENAME:		SHEET: 8 OF 10		DATE: 7/14/02	

APC

AMERICAN POWER
CONVERSION

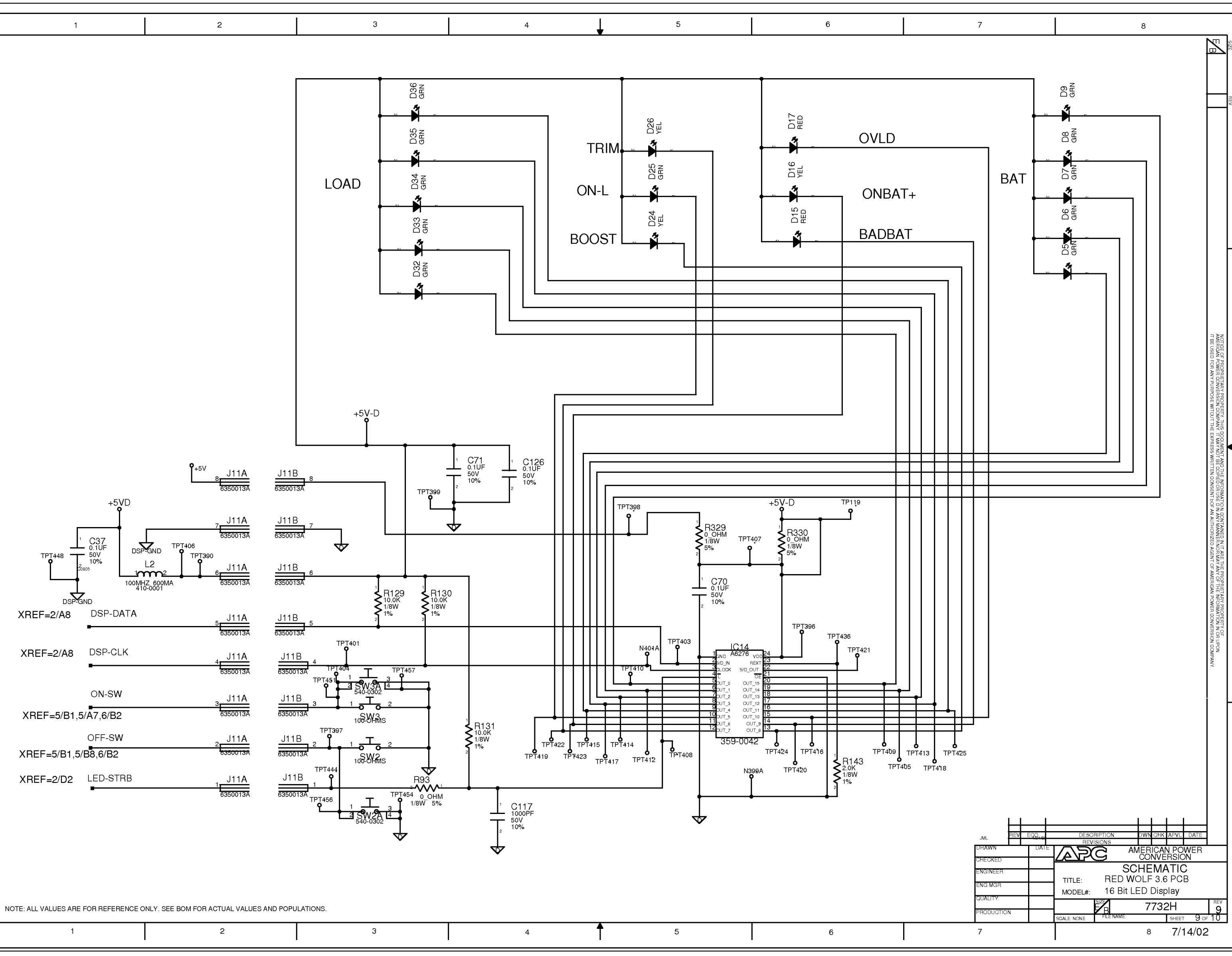
SCHEMATIC

TITLE: RED WOLF 3.6 PCB

MODEL#:

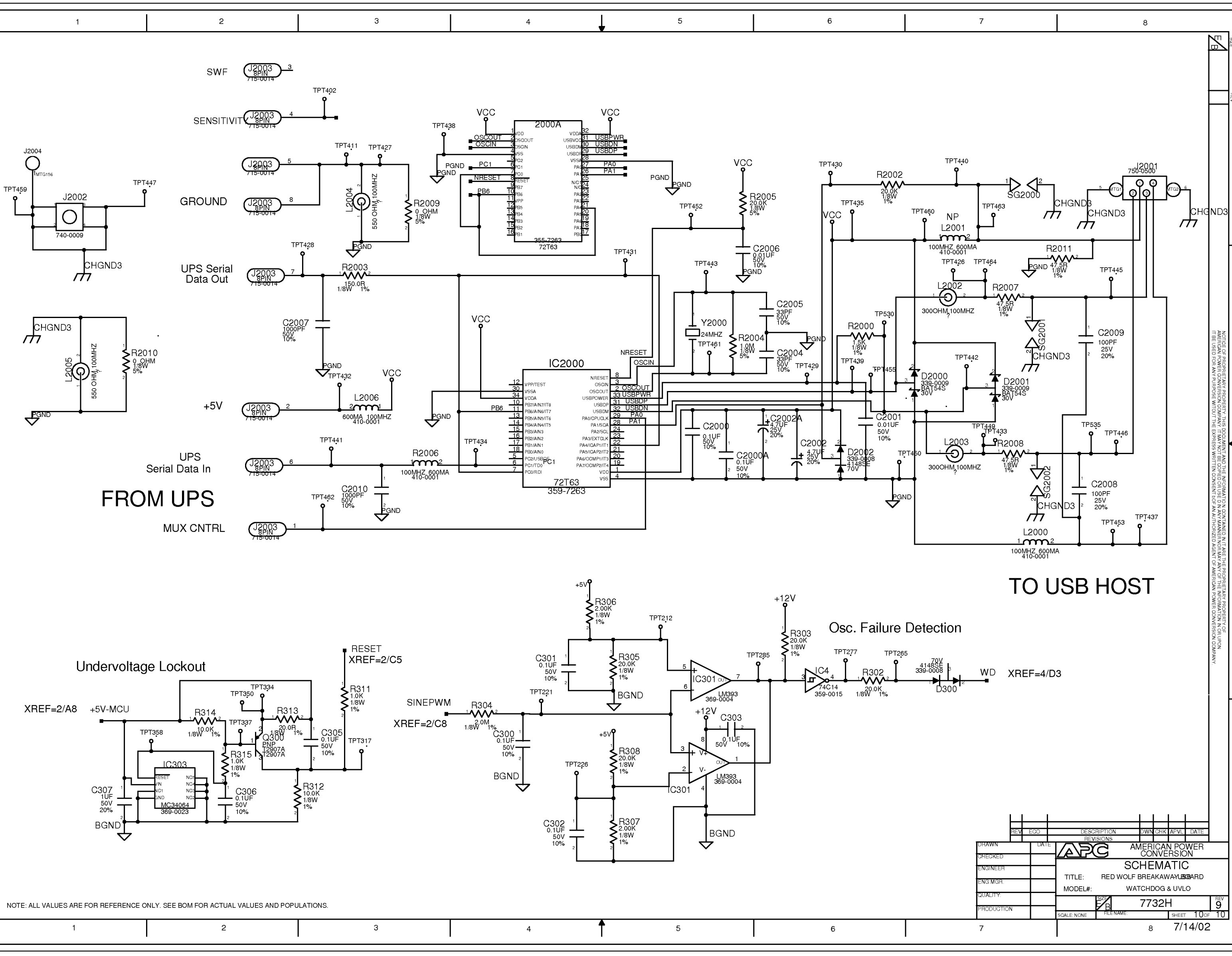
7732H

REV 9



NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

JML		REV		EQ		DESCRIPTION		OWN		CHK		APVL		DATE	
DRAWN		DATE		REV		DESCRIPTION		OWN		CHK		APVL		DATE	
CHECKED		DATE		REV		DESCRIPTION		OWN		CHK		APVL		DATE	
ENGINEER		DATE		REV		DESCRIPTION		OWN		CHK		APVL		DATE	
ENG. MGR		DATE		REV		DESCRIPTION		OWN		CHK		APVL		DATE	
QUALITY		DATE		REV		DESCRIPTION		OWN		CHK		APVL		DATE	
PRODUCTION		DATE		REV		DESCRIPTION		OWN		CHK		APVL		DATE	
SCALE: NONE		FILE NAME		SHEET		9 of 10		7/14/02							



NOTE: ALL VALUES ARE FOR REFERENCE ONLY. SEE BOM FOR ACTUAL VALUES AND POPULATIONS.

REV		ECO	DESCRIPTION	OWN	CHK	APVL	DATE
1			SCHEMATIC				
2			RED WOLF BREAKAWAY USB				
3			WATCHDOG & UVLO				
4			7732H				
5			9				
6			10 OF 10				
7			7/14/02				