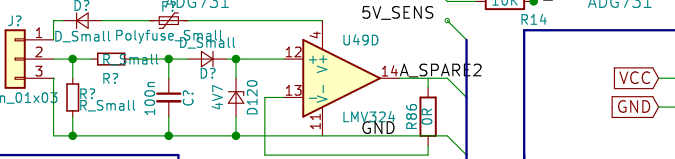


A OILT	12	S1	A Turr F 12	S1	VDD	13	VCC
A OILP	11	S2	A Turr S 11	S2	VDD	14	VCC
A COOLP	10	S3	A Turr R 10	S3	VDD		
A CRGT	9	S4	A Turl F 9	S4			
A CRGP	8	S5	A Turl S 8	S5			
A FUEL	6	S7	A LGTR F 6	S7	SYNC	17	!EN_ADMX1
A OUIT	5	S8	A LGTR R 5	S8	DIN	18	SPI_MOSI
A INTT	4	S9	A LGTR LIC	S9	SCLK	19	SPI_CLK
A PCBT	3	S10	A LGTL F 3	S10			
A BATT	2	S11	A LGTL R 2	S11			
A DPDP	1	S12	A HBMR F 1	S12			
A SCRD	47	S13	A HBMR A 47	S13			
A SPARE1	46	S14	A HBML A 46	S14			
	45	S15	A HBML 45	S15			
	46	S16	A LBMR 46	S16	D	43	ADC1_IN
	25	S17	A LBML 25	S17			
	26	S18	A FFR	S18			
	27	S19	A FFG	S19			
	28	S20	A RFG R 28	S20			
	29	S21	A REVR 29	S21			
	30	S22	A DRLL 30	S22			
	31	S23	A BRK L 31	S23			
	32	S24	A BRK R 32	S24			
	33	S25	A BRK 3 33	S25			
A Turr T 34	S26		A SPARE2 35	S26			
A Turl T 35	S27			S27			
A LGTR T 36	S28			S28			
A LGTL T 37	S29			S29			
A RFG T 38	S30			S30	GND	23	GND
A REVR T 39	S31			S31	VSS	24	GND
A BRK T 40	S32			S32			



A Turr T	↔	A_Turr_T
A Turl T	↔	A_Turl_T
A LGTR T	↔	A_LGTR_T
A LGTL T	↔	A_LGTL_T
A RFG T	↔	A_RFG_T
A REVR T	↔	A_REVR_T
A BRK T	↔	A_BRK_T
A Turr F	↔	A_Turr_F
A Turr S	↔	A_Turr_S
A Turr R	↔	A_Turr_R
A Turl F	↔	A_Turl_F
A Turl S	↔	A_Turl_S
A Turl R	↔	A_Turl_R
A LGTR F	↔	A_LGTR_F
A LGTR R	↔	A_LGTR_R
A LGTR LIC	↔	A_LGTR_LIC
A LGTL F	↔	A_LGTL_F
A LGTL R	↔	A_LGTL_R
A HBMR F	↔	A_HBMR_F
A HBMR A	↔	A_HBMR_A
A HBMR R	↔	A_HBMR_R
A HBMR F	↔	A_HBML_F
A HBMR A	↔	A_HBML_A
A LBMR	↔	A_LBMR
A LBML	↔	A_LBML
A FFR	↔	A_FFR
A FFG	↔	A_FFG
A RFG R	↔	A_RFG_R
A REVR R	↔	A_REVR_R
A DRLL	↔	A_DRLL
A BRK L	↔	A_BRK_L
A BRK R	↔	A_BRK_R
A BRK 3	↔	A_BRK_3
A FAN1	↔	A_FAN1
A FAN2	↔	A_FAN2

!IRD_TUR_TEMP	↔	!IRD_TUR_TEMP
!IRD_DPF_TEMP	↔	!IRD_DPF_TEMP
!IRD_SCR_TEMP	↔	!IRD_SCR_TEMP
!EN_ADMX1	↔	!EN_MUX1
!EN_ADMX2	↔	!EN_MUX2
SPI_MISO	↔	SPI_MISO
SPI_MOSI	↔	SPI_MOSI
SPI_CLK	↔	SPI_CLK
5V_SENS	↔	5V_SENS