



# 1.9 GHz BANDWIDTH GENERAL PURPOSE SILICON MMIC AMPLIFIER

T-74-13-01

UPC1675B  
UPC1675G  
UPC1675P

## FEATURES

- **WIDE BANDWIDTH:**  
1900 MHz TYP at 3 dB Point for UPC1675G  
2100 MHz TYP at 3 dB Point for UPC1675B, UPC1675P
- **HIGH ISOLATION**
- **SINGLE POWER SUPPLY:**  $V_{CC} = 5\text{ V}$
- **INPUT/OUTPUT MATCHED TO 50  $\Omega$**
- **LOW COST**

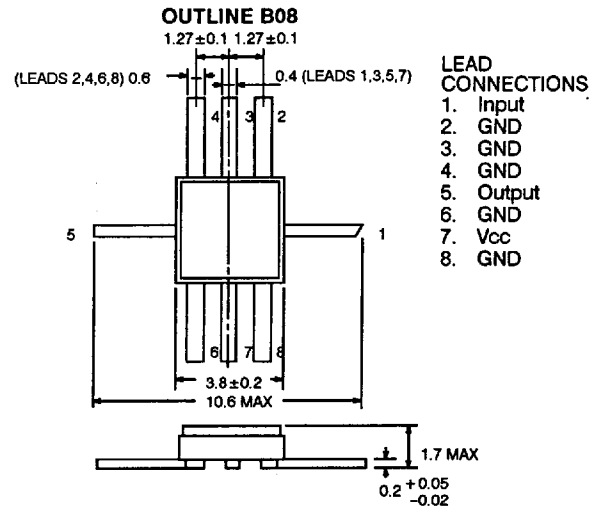
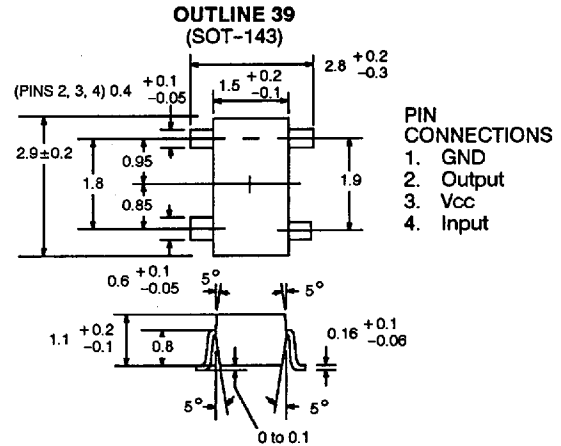
## DESCRIPTION

The UPC1675 is a silicon monolithic integrated circuit designed for wide-band amplifiers covering the HF to UHF bands. The series is available in two package styles: a surface mount package (UPC1675G), an 8 lead ceramic flat package (UPC1675B) and in chip form (UPC1675P).

## ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
V <sub>CC</sub>	Power Supply Voltage	V	6
P <sub>T</sub>	Total Power Dissipation UPC1675B UPC1675G	W mW	1.5 (T <sub>C</sub> = +125°C) 200
T <sub>OP</sub>	Operating Temperature UPC1675B/P UPC1675G	°C °C	-55 to +125 -40 to +85
T <sub>STG</sub>	Storage Temperature UPC1675B/P UPC1675G	°C °C	-65 to +200 -55 to +150

## OUTLINE DIMENSIONS (Units in mm)



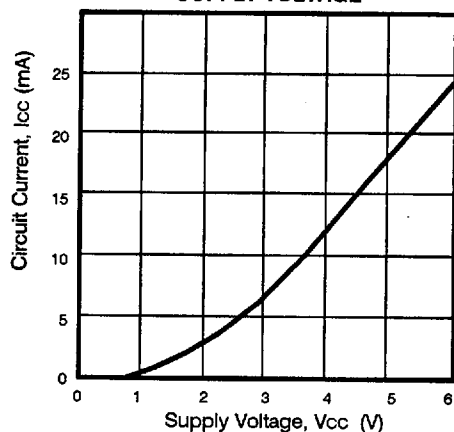
## ELECTRICAL CHARACTERISTICS (TA = 25°C)

PART NUMBER PACKAGE OUTLINE			UPC1675B* B08			UPC1675G 39			UPC1675P 00 (CHIP)		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX
I <sub>CC</sub>	Supply Current at V <sub>CC</sub> = 5 V	mA	12	17	22	12	17	22	12	17	22
S <sub>21</sub>   <sup>2</sup>	Power Gain at V <sub>CC</sub> = 5 V, f = 500 MHz	dB	10	12	14	10	12	14	10	12	14
NF	Noise Figure at V <sub>CC</sub> = 5 V, f = 500 MHz	dB		5.5	7		5.5	7		5.5	7
BW	Bandwidth at V <sub>CC</sub> = 5 V, 3 dB down	MHz	1800	2100			1900		1800	2100	
S <sub>12</sub>	Isolation at V <sub>CC</sub> = 5 V, f = 500 MHz	dB	21	25			24.5				
S <sub>11</sub>	Input Return Loss at V <sub>CC</sub> = 5 V, f = 500 MHz	dB	9	12		9	12		9	12	
S <sub>22</sub>	Output Return Loss at V <sub>CC</sub> = 5 V, f = 500 MHz	dB	10	13		8	12				
P <sub>OUT</sub>	Maximum Output Power at V <sub>CC</sub> = 5 V, f = 500 MHz	dBm	2	4		2	4.5		2	4.5	

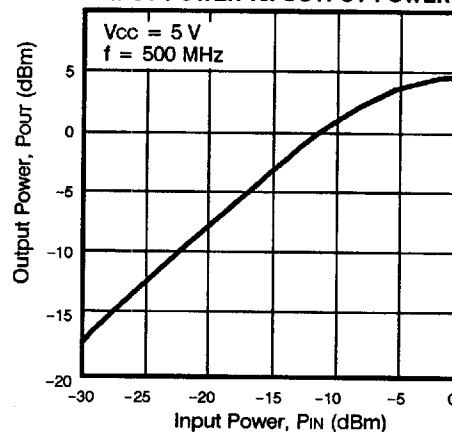
\*Case shall be connected to GND for stable RF operation and better thermal dissipation.

TYPICAL PERFORMANCE CHARACTERISTICS (TA = 25°C)

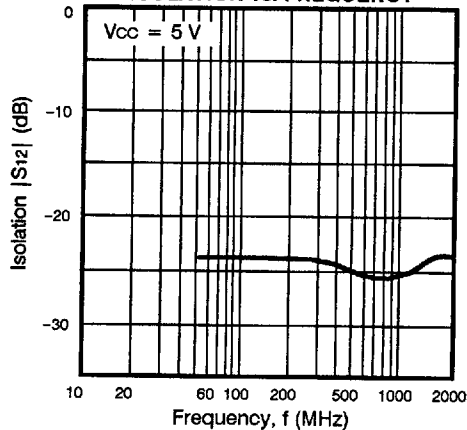
CIRCUIT CURRENT vs. SUPPLY VOLTAGE



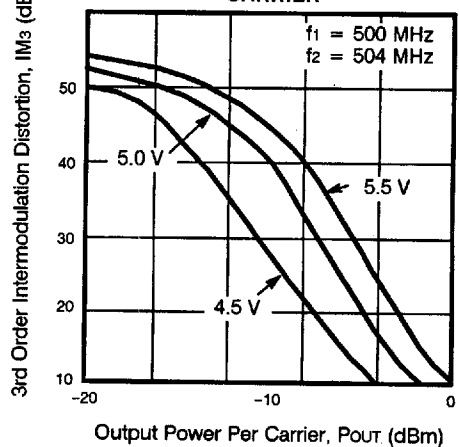
INPUT POWER vs. OUTPUT POWER



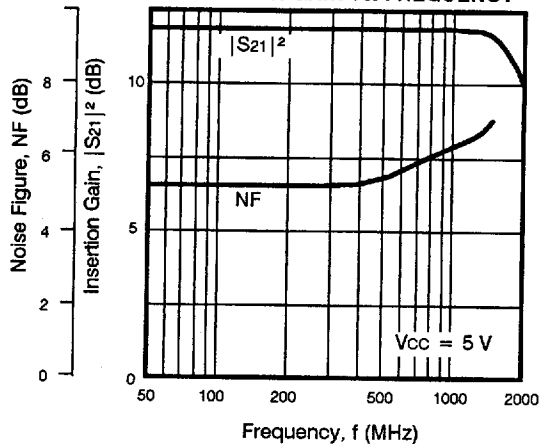
UPC1675B/P ISOLATION vs. FREQUENCY



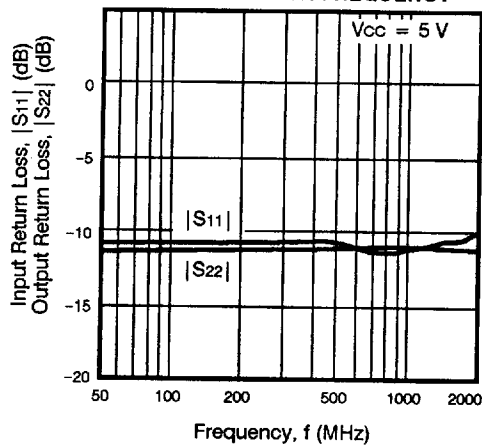
UPC1675B/P THIRD ORDER INTERMODULATION DISTORTION vs. OUTPUT POWER PER CARRIER



UPC1675B/P NOISE FIGURE AND INSERTION GAIN vs. FREQUENCY



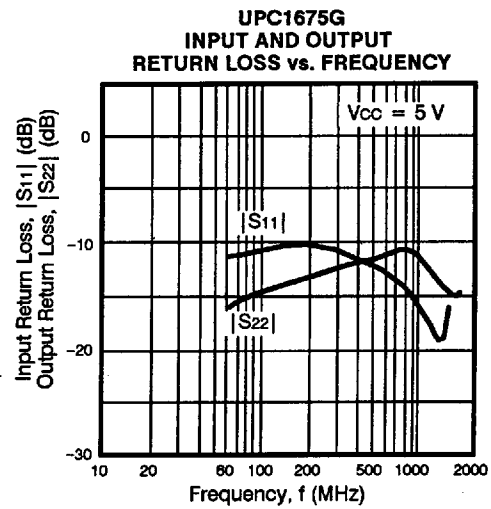
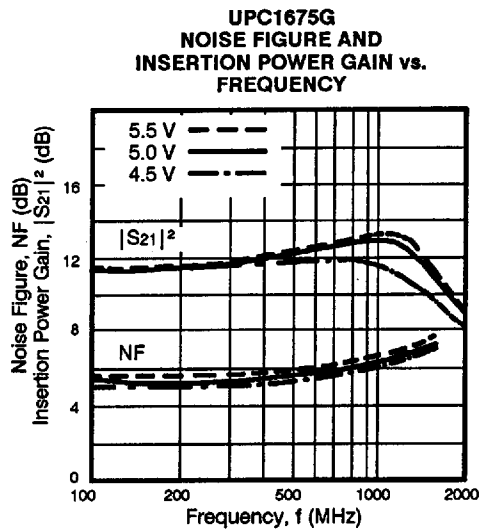
UPC1675B/P INPUT AND OUTPUT RETURN LOSS vs. FREQUENCY



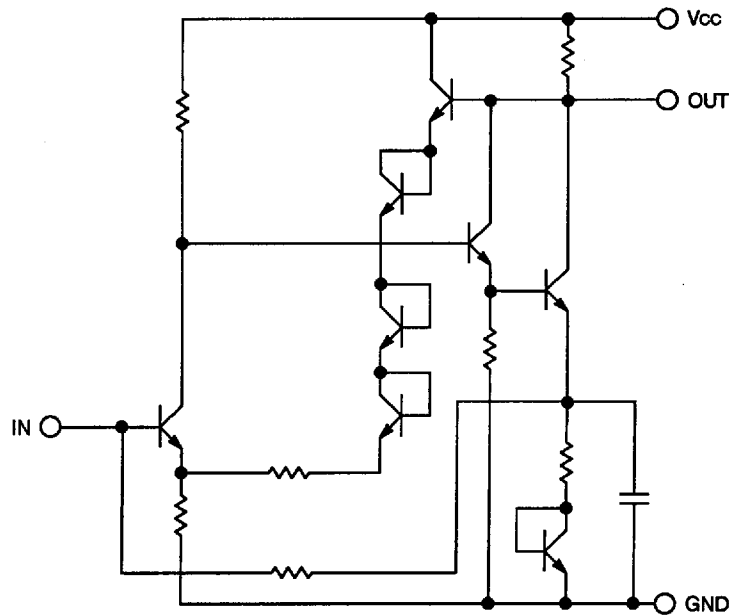
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**UPC1675B, UPC1675G, UPC1675P**

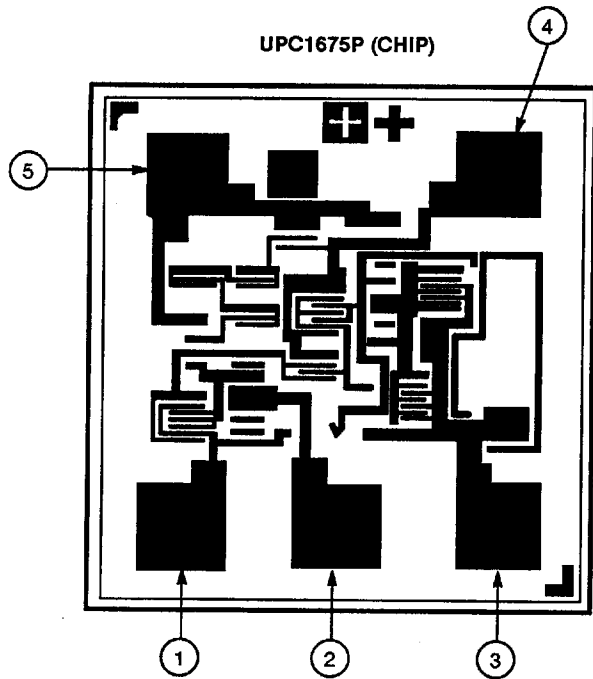
**TYPICAL PERFORMANCE CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$ )



**EQUIVALENT CIRCUIT**



**CHIP DIMENSIONS**



- DIE SIZE  
0.60 mm x 0.65 mm
- THICKNESS  
170 ± 30 μm
- TI/Pt/Au  
METALLIZATION
- PAD SIZE  
100 μm x 100 μm

BONDING PAD NO.	BONDING PAD POSITION (μm)		PAD CONNECTION
	X AXIS	Y AXIS	
1	-185	-210	INPUT
2	-5	-210	GND
3	+185	-210	GND
4	+185	+210	OUTPUT
5	-175	+210	Vcc